

ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

Title of Invention

A METHOD, SYSTEM, AND COMPUTER PROGRAM PRODUCT
FOR AUTOMATIC INSERTION AND CORRECTNESS
VERIFICATION OF LEVEL SHIFTERS IN INTEGRATED CIRCUITS
WITH MULTIPLE VOLTAGE DOMAINS

Submission Type : Utility Patent Filing

Application Number:

10/711971



EFS ID:

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First Named Applicant: Bhanu KAPOOR

Attorney Docket Number: Q83911

Timestamp: 2004-10-15 21:18:43 EDT

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us-fee-sheet	Q83911-usfees.xml	1960	2004-10-15
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